January 8, 2018

## **Revision History**

Item No.	Release Date	Device	Country	Comm. Protoco		
Commentary						
FL-TW1120	07/13/16	(M)FL	Taiwan	CCNET		
1. Processing	of the old TWD 500 and 10	00 notes without a	holographic strip has been	disabled.		
	eptance rate has been imp					
3. Greeting m	essage has been changed f	rom "CashCode" to	"SUZOHAPP".			
FL-TW1519	12/24/09	(M)FL	Taiwan	CCNET		
Overall accepta	ance has been improved ba	sed on the latest sai	nples of street grade bills.			
FL-TW1518	08/31/09	(M)FL	Taiwan	CCNET		
•	e of 1000 TWD has been im gorithm has been modified	•		grade bills.		
FL-TW1517	10/15/08	(M)FL	Taiwan	CCNET		
	e of 500 TWD has been imp					
<ol> <li>Checking o entry slot.</li> </ol>	f drop box presence has be					
entry slot.	05/02/08	(M)FL	Taiwan	CCNET		
entry slot. FL-TW1516 1. Counterfei	05/02/08 t protection has been impro	(M)FL	Taiwan	CCNET		
entry slot. FL-TW1516 1. Counterfei counterfeit	05/02/08 t protection has been impro	(M)FL oved according to th	Taiwan e latest samples of 100 &	CCNET		
entry slot. FL-TW1516 1. Counterfei counterfeit 2. Stacker cyc	05/02/08 t protection has been impro	(M)FL oved according to th g from stacking stat	Taiwan e latest samples of 100 & e.	CCNET 1000 TWD		
entry slot. FL-TW1516 1. Counterfeit counterfeit 2. Stacker cyc 3. Rejecting c insertions.	05/02/08 t protection has been improtes. cle added in case of rejectin ode "Conv. error"(0x64) ha	(M)FL oved according to th g from stacking stat s been replaced with	Taiwan e latest samples of 100 & e. n "Insertion error"(0x60) fo	CCNET 1000 TWD		
entry slot. FL-TW1516 1. Counterfeit 2. Stacker cyc 3. Rejecting c insertions. 4. Pause state	05/02/08 t protection has been impro ts. cle added in case of rejectin ode "Conv. error"(0x64) ha e will occur if both Input an	(M)FL oved according to th g from stacking stat s been replaced with d Aligning sensors co	Taiwan e latest samples of 100 & e. n "Insertion error"(0x60) fo	CCNET 1000 TWD		
entry slot. FL-TW1516 1. Counterfeit 2. Stacker cyc 3. Rejecting c insertions. 4. Pause state	05/02/08 t protection has been improtes. cle added in case of rejectin ode "Conv. error"(0x64) ha	(M)FL oved according to th g from stacking stat s been replaced with d Aligning sensors co	Taiwan e latest samples of 100 & e. n "Insertion error"(0x60) fo	CCNET 1000 TWD		
entry slot. FL-TW1516 1. Counterfeit 2. Stacker cyc 3. Rejecting c insertions. 4. Pause state 5. 10 sec. tim	05/02/08 t protection has been impro ts. cle added in case of rejectin ode "Conv. error"(0x64) ha e will occur if both Input an	(M)FL oved according to th g from stacking stat s been replaced with d Aligning sensors co	Taiwan e latest samples of 100 & e. n "Insertion error"(0x60) fo	CCNET 1000 TWD		
entry slot. FL-TW1516 1. Counterfeit 2. Stacker cyc 3. Rejecting c insertions. 4. Pause state 5. 10 sec. tim FL-TW1515	05/02/08 t protection has been improtes. cle added in case of rejectin ode "Conv. error"(0x64) ha e will occur if both Input and eout implemented in Pause	(M)FL oved according to th g from stacking stat s been replaced with d Aligning sensors co e state. (M)FL	Taiwan e latest samples of 100 & e. n "Insertion error"(0x60) fo overed. Taiwan	CCNET 1000 TWD or wrong CCNET		
entry slot. FL-TW1516 1. Counterfeit 2. Stacker cyc 3. Rejecting c insertions. 4. Pause state 5. 10 sec. tim FL-TW1515	05/02/08 t protection has been improtes. cle added in case of rejectin ode "Conv. error"(0x64) ha e will occur if both Input and eout implemented in Pause 05/18/06	(M)FL oved according to th g from stacking stat s been replaced with d Aligning sensors co e state. (M)FL	Taiwan e latest samples of 100 & e. n "Insertion error"(0x60) fo overed. Taiwan	CCNET 1000 TWD or wrong CCNET		
entry slot. FL-TW1516 1. Counterfeit 2. Stacker cyc 3. Rejecting c insertions. 4. Pause state 5. 10 sec. tim FL-TW1515 Acceptance rat FL-TW1514	05/02/08 t protection has been improtes. cle added in case of rejectin ode "Conv. error"(0x64) ha e will occur if both Input and eout implemented in Pause 05/18/06 te of 500 TWD has been imp	(M)FL oved according to the g from stacking stat s been replaced with d Aligning sensors co e state. (M)FL proved according to (M)FL	Taiwan e latest samples of 100 & e. n "Insertion error"(0x60) fo overed. Taiwan the latest samples of stree Taiwan	CCNET 1000 TWD or wrong CCNET et grade bills.		
entry slot. FL-TW1516 1. Counterfeit 2. Stacker cyc 3. Rejecting c insertions. 4. Pause state 5. 10 sec. tim FL-TW1515 Acceptance rat FL-TW1514 Overall accepta	05/02/08 t protection has been improtes. cle added in case of rejectin ode "Conv. error"(0x64) ha e will occur if both Input and eout implemented in Pause 05/18/06 ce of 500 TWD has been imp 11/22/05	(M)FL oved according to the g from stacking stat s been replaced with d Aligning sensors co e state. (M)FL proved according to (M)FL	Taiwan e latest samples of 100 & e. n "Insertion error"(0x60) fo overed. Taiwan the latest samples of stree Taiwan	CCNET 1000 TWD or wrong CCNET et grade bills.		
entry slot. FL-TW1516 1. Counterfeit 2. Stacker cyc 3. Rejecting c insertions. 4. Pause state 5. 10 sec. tim FL-TW1515 Acceptance rat FL-TW1514 Overall accepta FL-TW1513 1. Prevention	05/02/08 t protection has been impro- ts. cle added in case of rejectin ode "Conv. error"(0x64) ha e will occur if both Input and eout implemented in Pause 05/18/06 ce of 500 TWD has been improve 11/22/05 ance rate has been improve	(M)FL oved according to the g from stacking stat s been replaced with d Aligning sensors co e state. (M)FL oroved according to (M)FL ed based on the bills (M)FL bill returning or reje	Taiwan e latest samples of 100 & e. n "Insertion error"(0x60) fo overed. Taiwan the latest samples of stree Taiwan from the customer. Taiwan ecting has been implement	CCNET 1000 TWD or wrong et grade bills. CCNET CCNET		

## **S**UZOHAPP

## January 8, 2018

Metal bezel support and auto-detection have been added

<u>FL-TW1511</u>	10/26/04	(M)FL	Taiwan	CCNET		
Acceptance has been enhanced according to the latest samples of street money.						
<u>FL-TW1501</u>	01/28/04	(M)FL	Taiwan	CCNET		

First release.