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Revision History

No	Item No.	File Name	Commentary	Release Date	Device	Country	Comm. Protocol
1	FL-CL1520T	FL-CL5AA	J1-4.20T.hex Overall acceptance ir	02/17/12 nproved (-	Chile comer bills and validation head.	CCNET
2	FL-CL1519	FL-CL5AA	· ·	tic 2000 (P issued CLP issue	Chile in 2011 has been introduced. d in 2010 has been improved ade bills.	CCNET
3	FL-CL1518	FL-CL5AA	J1-4.18.hex 1. Processing of new 2. Processing of 500		P and 200	Chile 200 CLP issued in 2010 has been abled.	CCNET introduced.
4	FL-CL1616	FL-CL6AA	J1-4.16.hex Processing of new 10	03/22/10 0000 CLP	-	Chile 2009 has been introduced.	CCNET
5	<u>FL-CL1615</u>	FL-CL6AA	J1-4.15.hex Processing of new 50	11/02/09 000 Pesos	-	Chile n 2009 has been added.	CCNET
6	FL-CL1614	FL-CL6AA	J1-4.14.hex Overall acceptance h bills.	09/02/09 as been i		Chile based on the latest samples of str	CCNET reet grade
7	<u>FL-CL1613</u>	FL-CL6AA	2. DIP switch to enab3. Synchronization by	ole / disab / transluc	LP issued le old 200 ent optic s	Chile I in 2008 has been introduced. O CLP has been brought in. signals has been implemented insparent plastic 2000 CLP bills.	CCNET
8	<u>FL-CL1612</u>	FL-CL6AA	in stacking state ins 2. Stacker cycle has a 3. Rejecting code "Code" "Insertion error" (0x6 4. Pause state will och 5. 10 sec timeout imp	stead of libeen add onv. error 60) for wrecur only in the or drop bor drop bo	een chang nput2 sen ed in case "(0x64) ha ong insert f both Inp d in Pause ox presence	e of rejecting from stacking state. as been replaced with iions. ut and Aligning sensors covered.	
9	<u>FL-CL1611</u>	FL-CL6AA	J1-4.11.hex Overall acceptance h grade bills.	10/29/07 as been i		Chile according to the latest samples of	CCNET
10	FL-CL1610	FL-CL6AA	J1-4.10.hex First release.	07/17/07	FL	Chile	CCNET